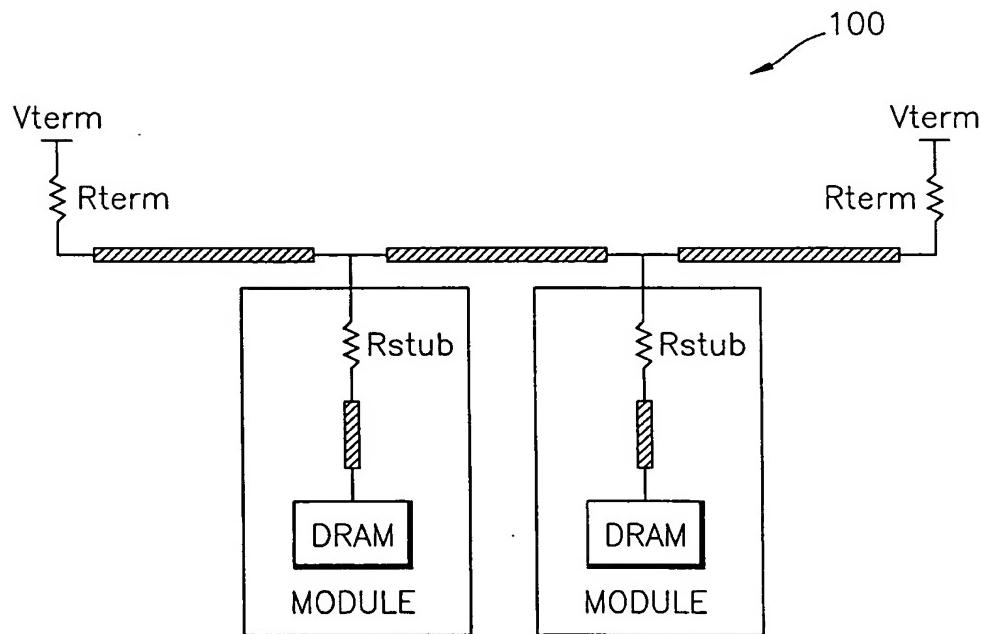


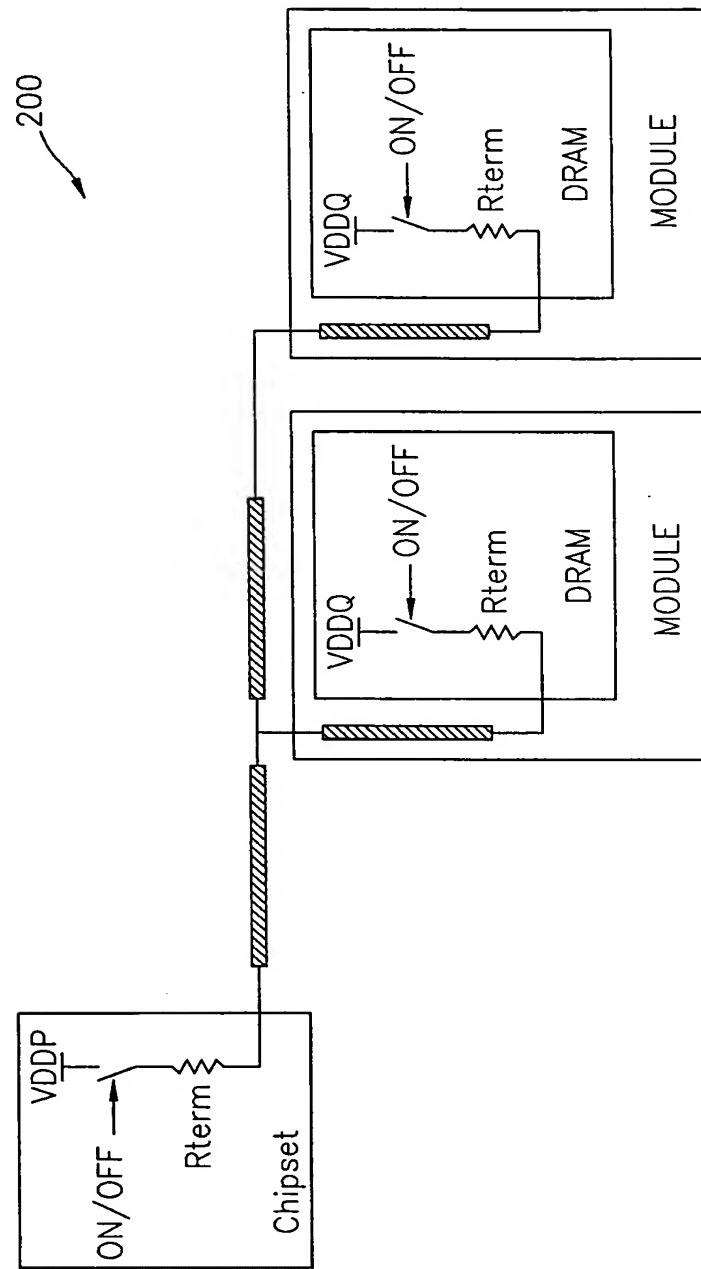
DEVICES AND METHODS FOR CONTROLLING ACTIVE TERMINATION RESISTORS IN A
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Inventor: Kye-Hyun KYUNG

FIG. 1 (PRIOR ART)



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FIG. 2 (PRIOR ART)



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FIG. 3

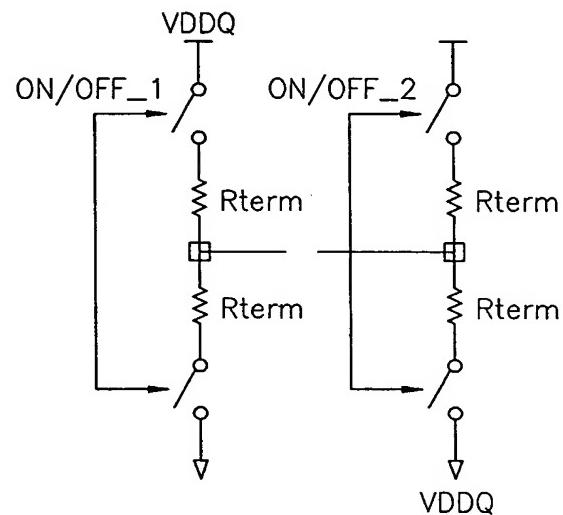
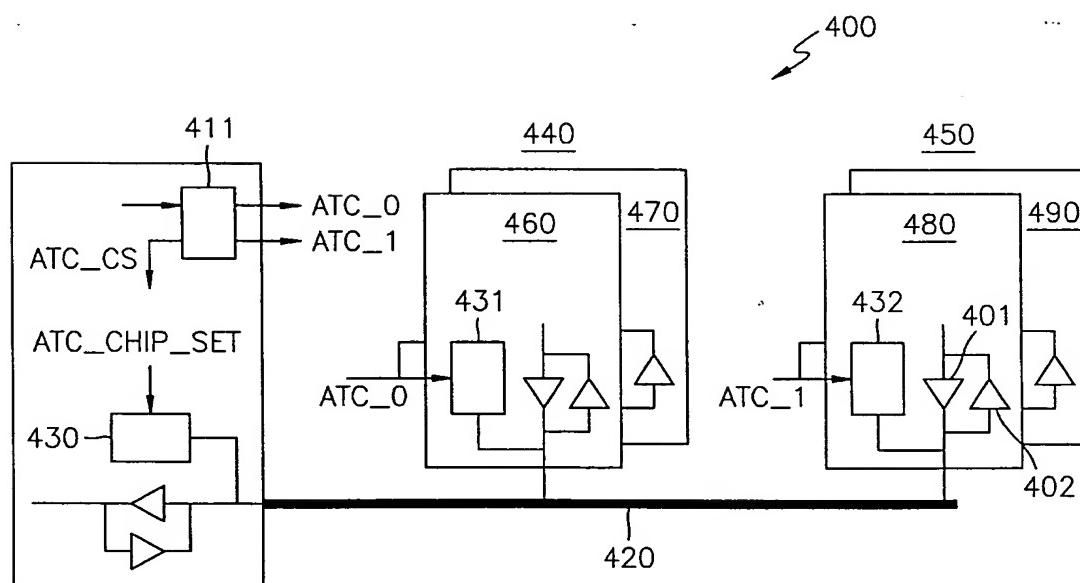


FIG. 4



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FIG. 5A

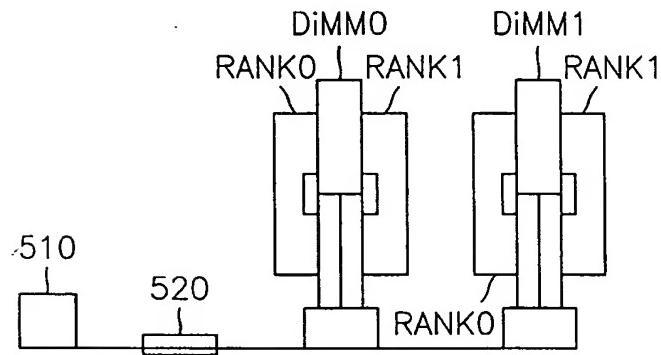
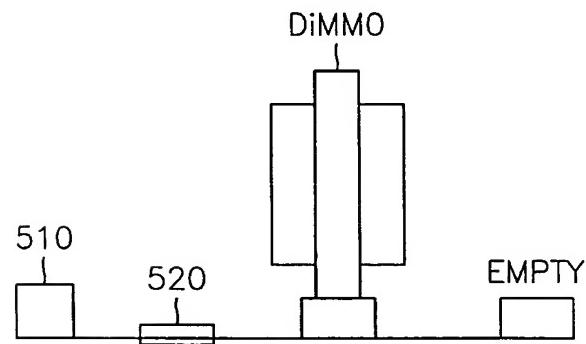
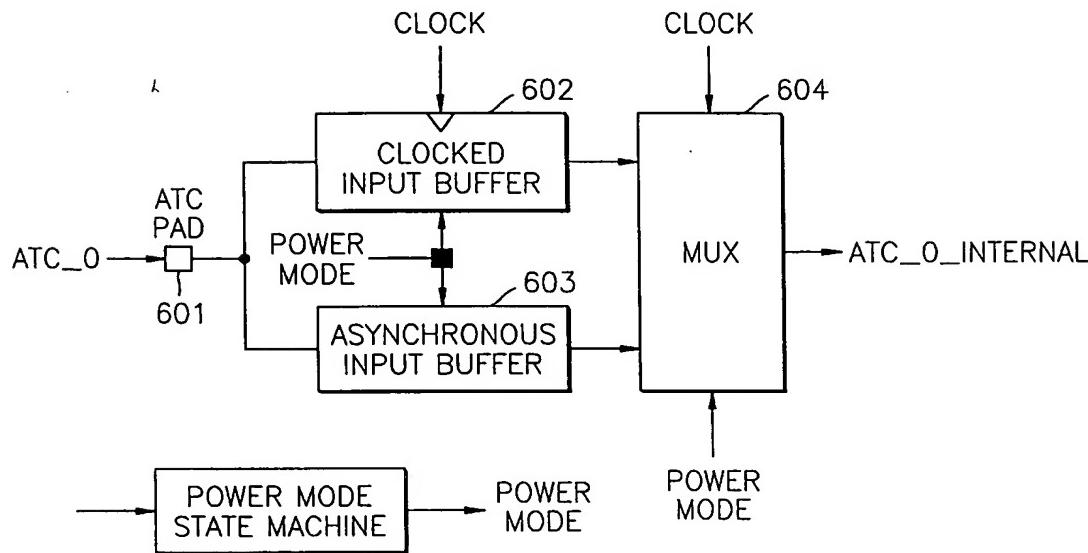


FIG. 5B



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FIG. 6



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FIG. 7A

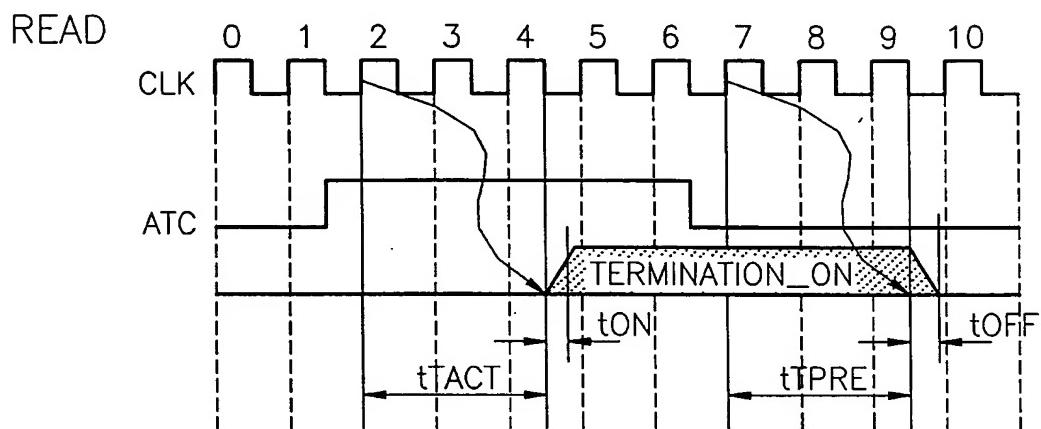
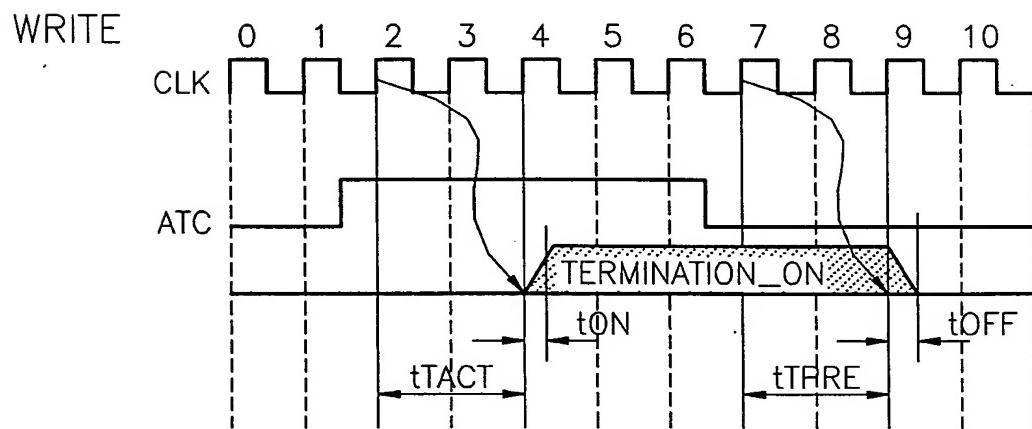
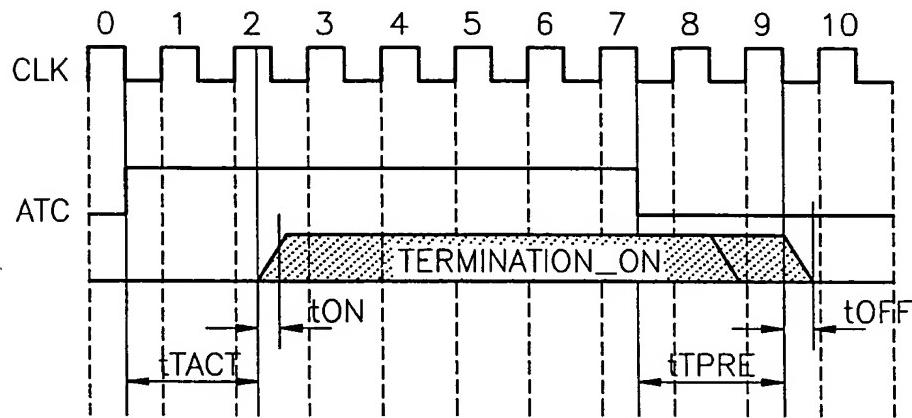


FIG. 7B



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FIG. 8



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FIG. 9A

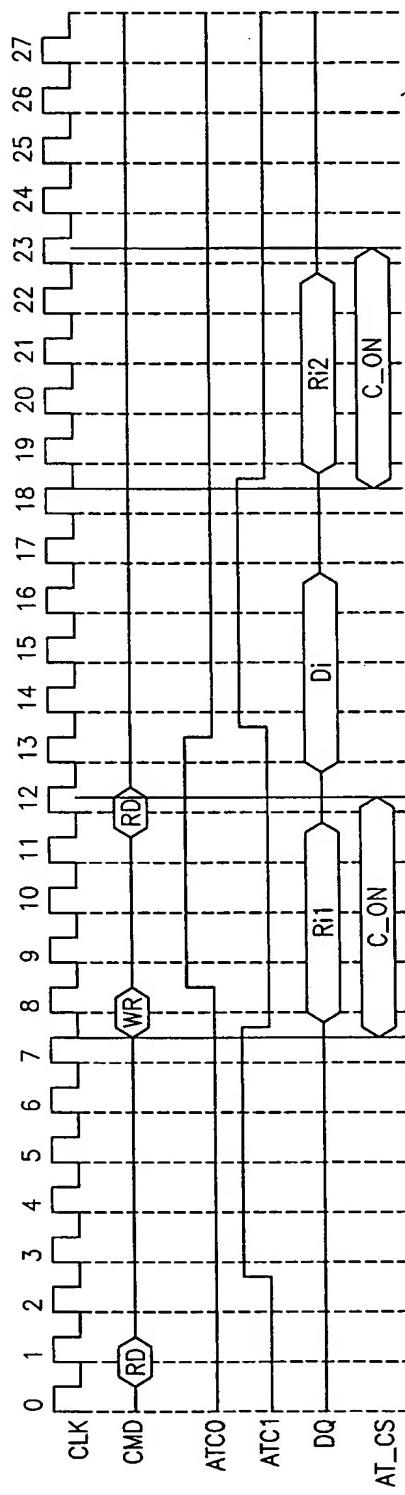
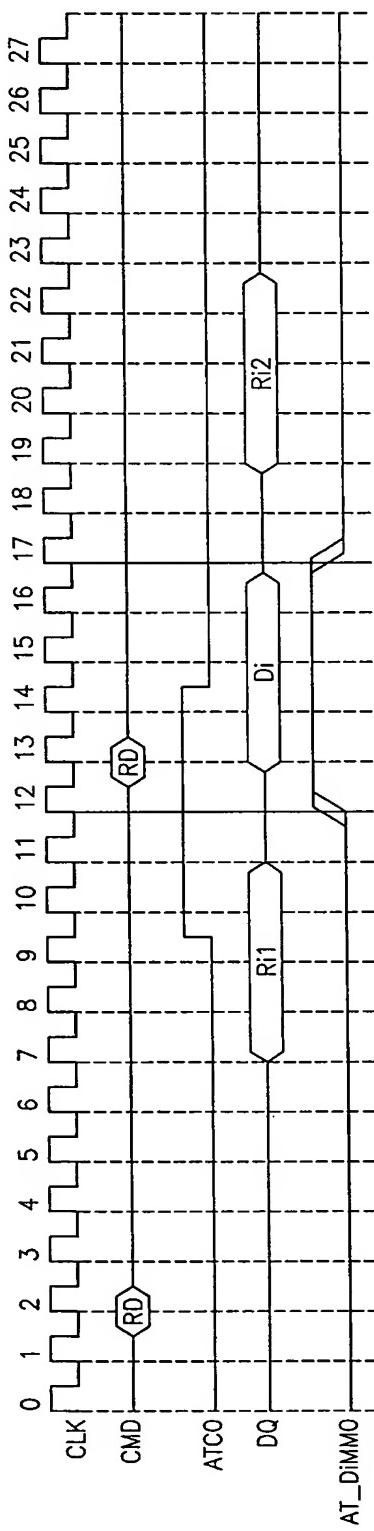


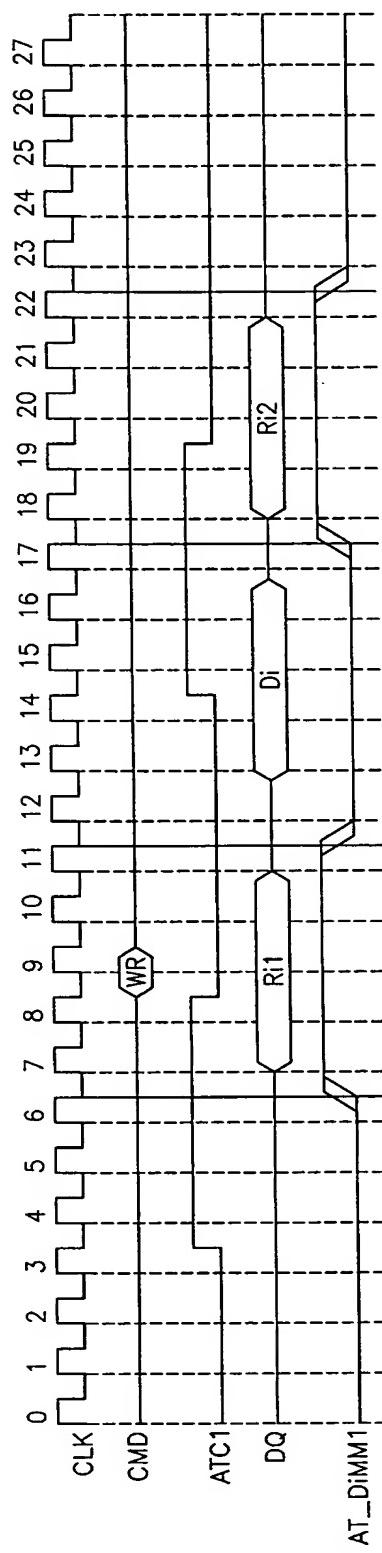
FIG. 9B



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FIG. 9C



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FIG. 10A

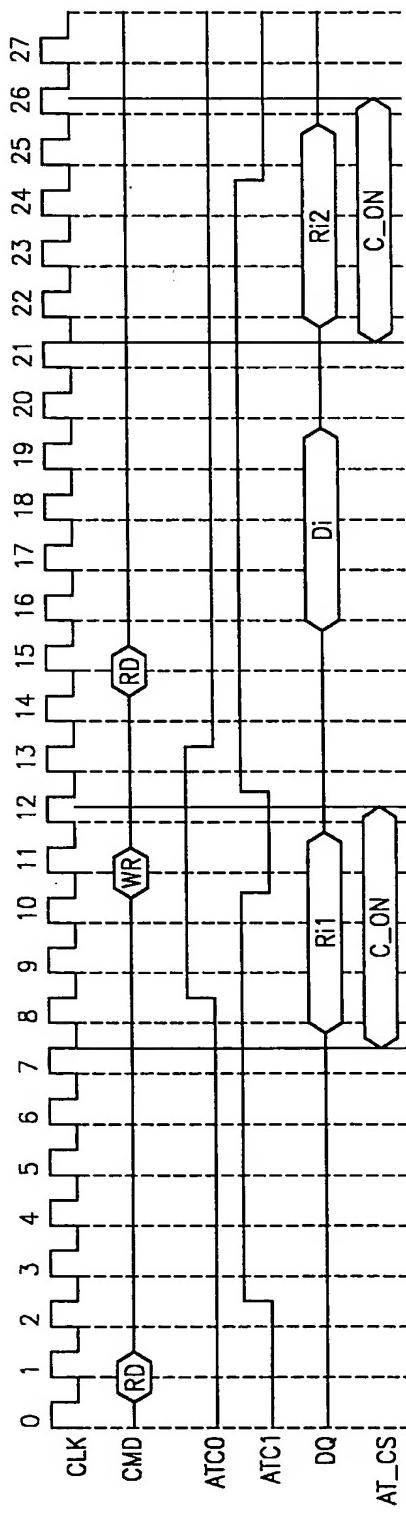
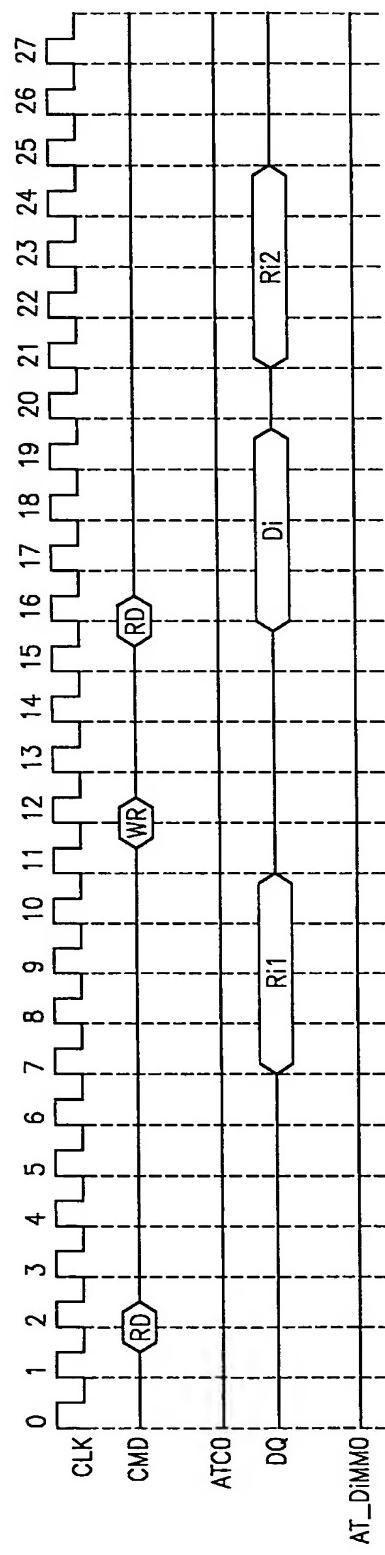
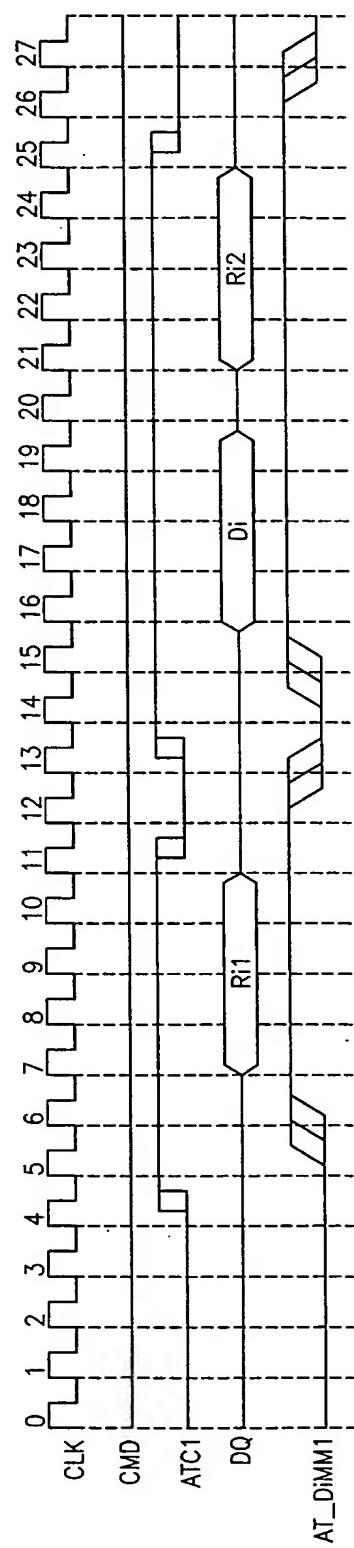


FIG. 10B



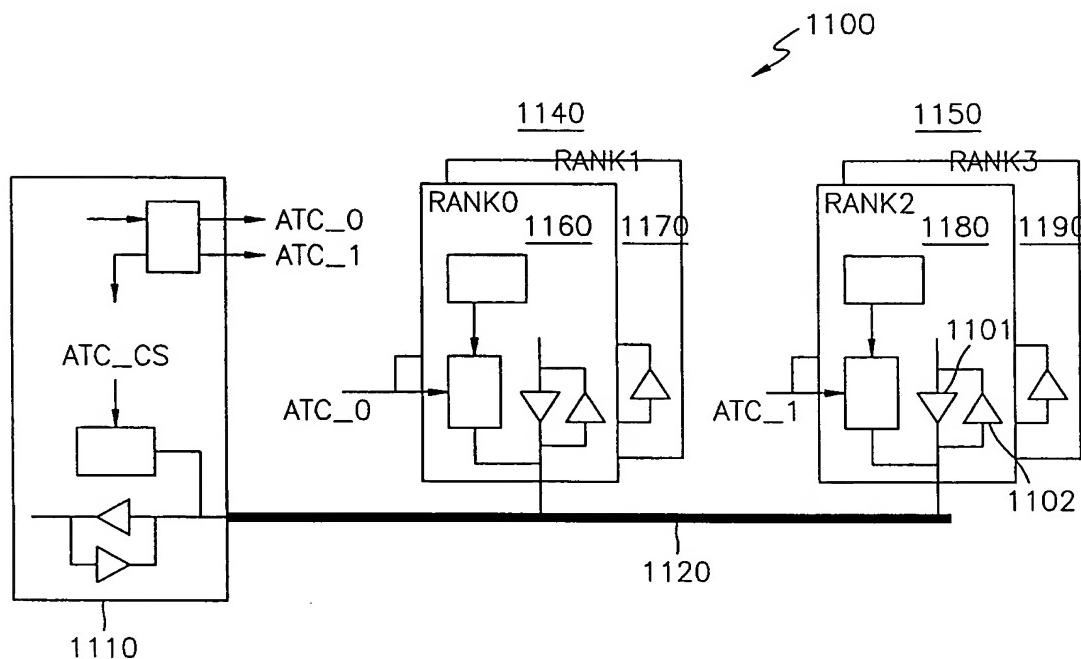
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FIG. 10C



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FIG. 11



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FIG. 12

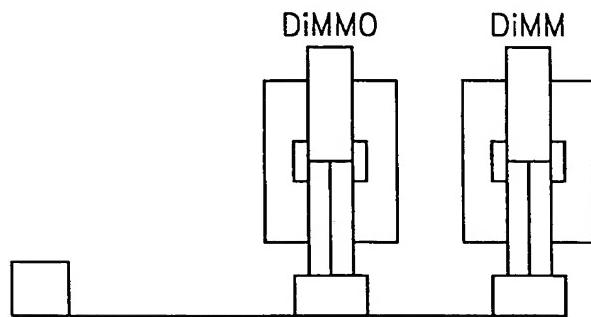


FIG. 13

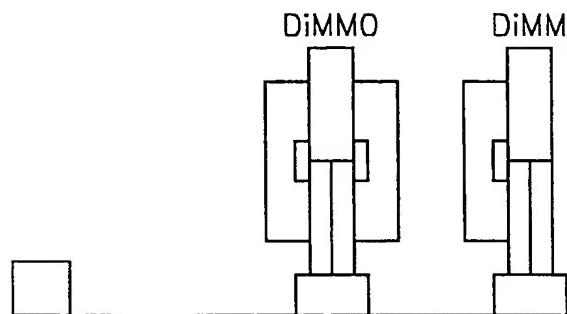
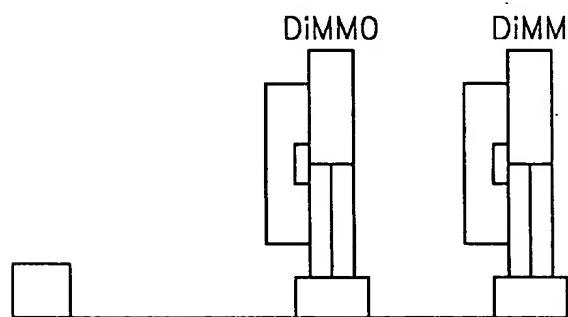


FIG. 14



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FIG. 15

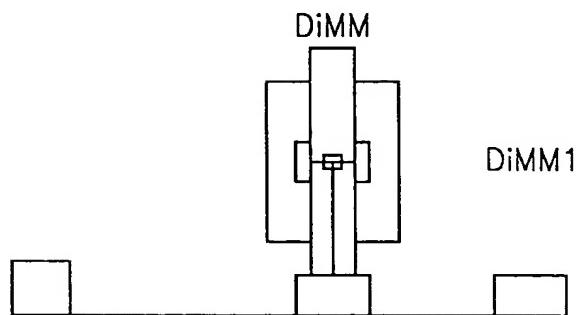


FIG. 16

